XA-9950
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Tsuyoshi ARIGANE et al.

Appln. No.: Group Art Unit:

Filed: HEREWITH

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The documents on the attached List were cited in the specification, on page 3, and their relevance is indicated therein.

Respectfully submitted,

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By: _____
Mitchell W. Shapiro
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October 10, 2003

FORM PTO-1449 LIST OF DOCUMENTS CITED BY APPLICANT	Atty. Docket No. XA-9950	Appln. No.
	Applicant Tsuyoshi ARIGANE et al.	
	Filing Date HEREWITH	Group

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
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	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

xaminer nitial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	02-074069	3/14/90	Japan			Abstract
	AK	08-046159	2/16/96	Japan			Abstract
	AL						
	AM						
	AN						
	AO						

OTHER (including author, title, date, pertinent pages, etc.)

	AP	
	AQ	
	AR	

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XAMINER: Initial if reference considered, whether or not citation is in conformance ith MPEP 609; draw line through citation if not in conformance and not considered. nclude copy of this form with next communication to Applicant.